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APPLICANTS Sadayoshi Horii, Tokyo, JAPAN; Hironobu Miya, Tokyo, JAPAN; Yoshiaki Hashiba, Tokyo, JAPAN; ** CONTINUING DATA ***** This application is a 371 of PCT/JP03/11988 09/19/2003 ** FOREIGN APPLICATIONS ***** JAPAN 2002-276076 09/20/2002 ** IF REQUIRED, FOREIGN FILING LICENSE GRANTED ** 07/18/2006						
Foreign Priority claimed <input checked="" type="checkbox"/> Yes <input type="checkbox"/> No	35 USC 119(a-d) conditions met <input checked="" type="checkbox"/> Yes <input type="checkbox"/> No	<input type="checkbox"/> Met after Allowance	STATE OR COUNTRY JAPAN	SHEETS DRAWINGS 6	TOTAL CLAIMS 20	INDEPENDENT CLAIMS 7
Verified and Acknowledged	/MAHMOUD DAHIMENE/ Examiner's Signature		Initials			
ADDRESS KRATZ, QUINTOS & HANSON, LLP 1420 K Street, N.W. Suite 400 WASHINGTON, DC 20005 UNITED STATES						
TITLE Method for manufacturing semiconductor device and substrate processing apparatus						
FILING FEE RECEIVED 1830	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:		<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit			